

FIG. 2

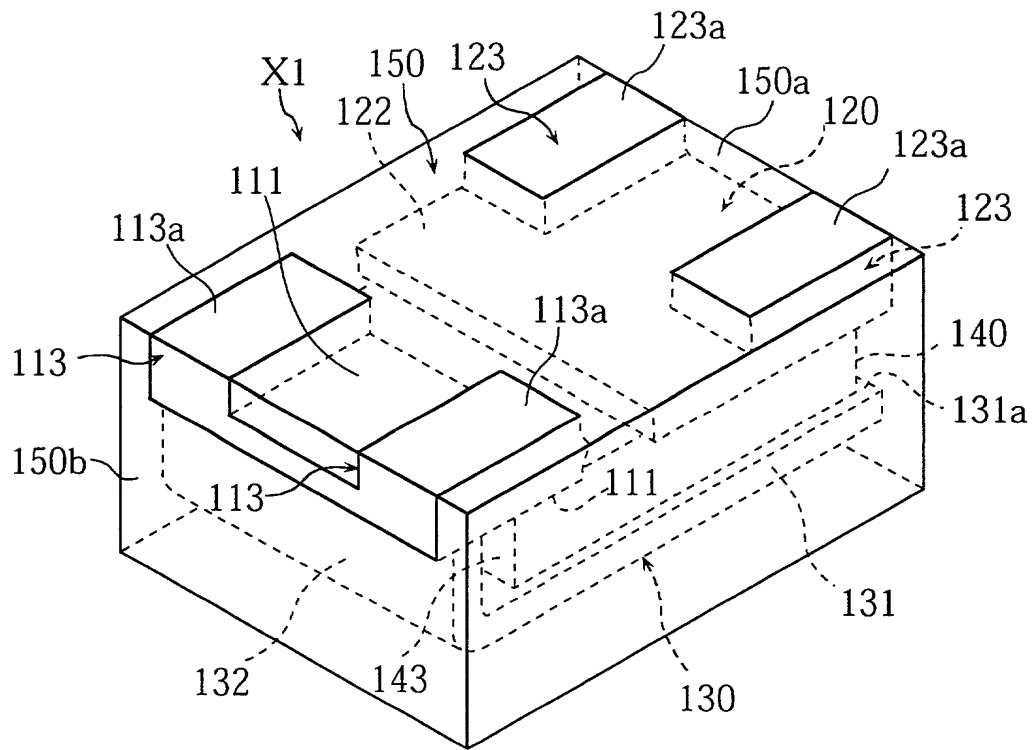
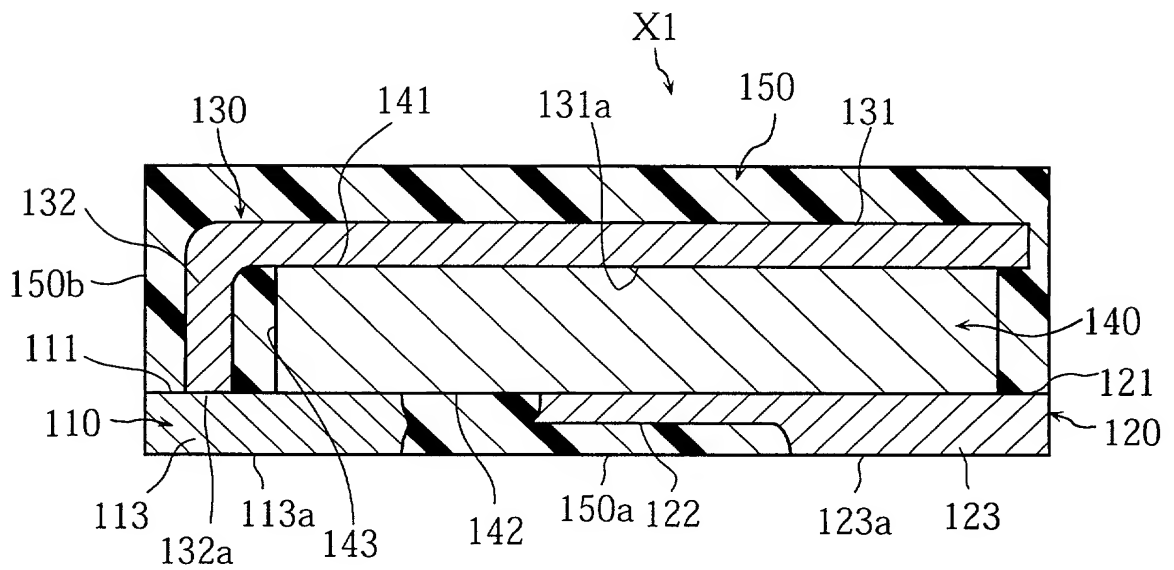


FIG. 3



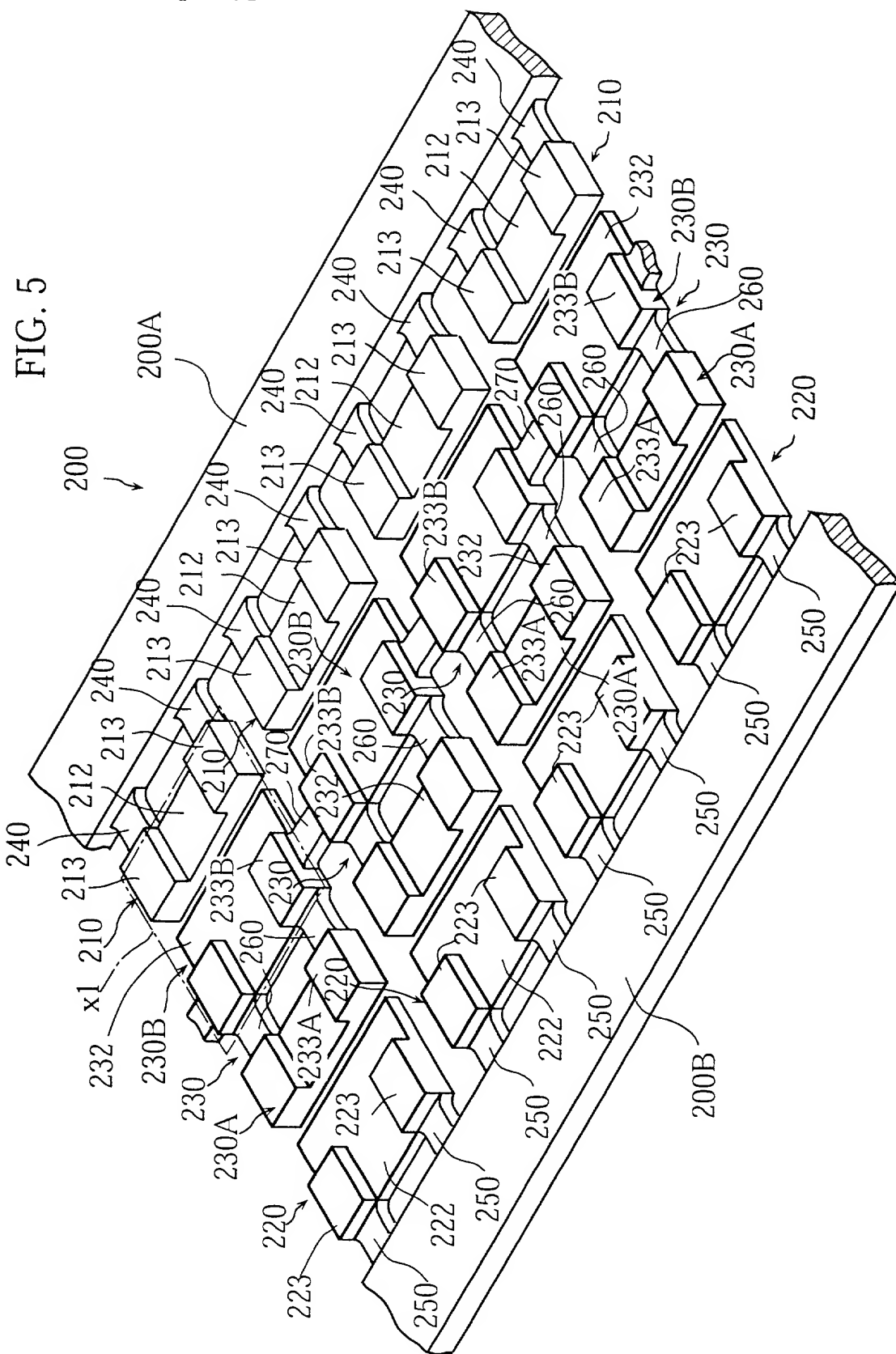


FIG. 6

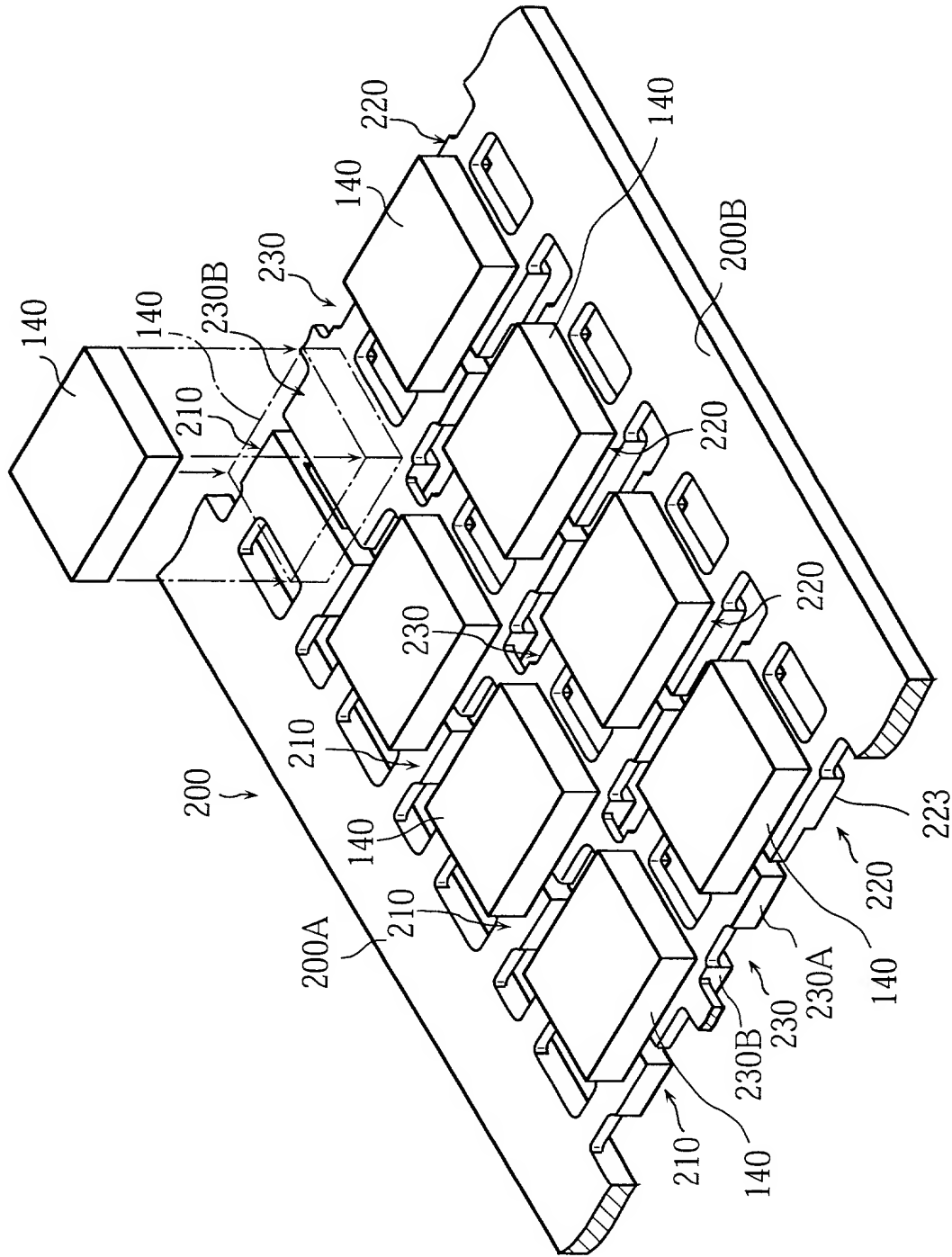


FIG. 7

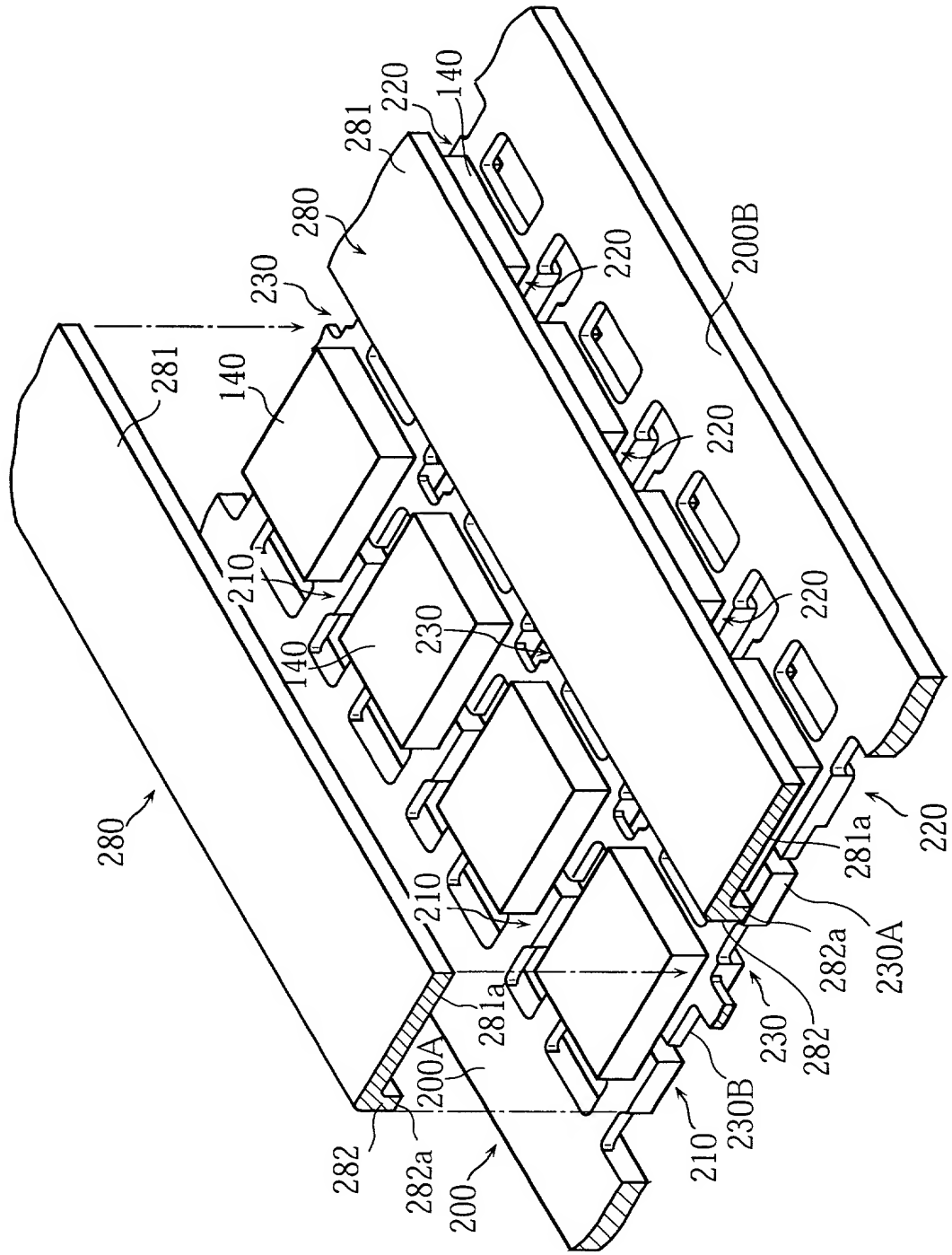


FIG. 8

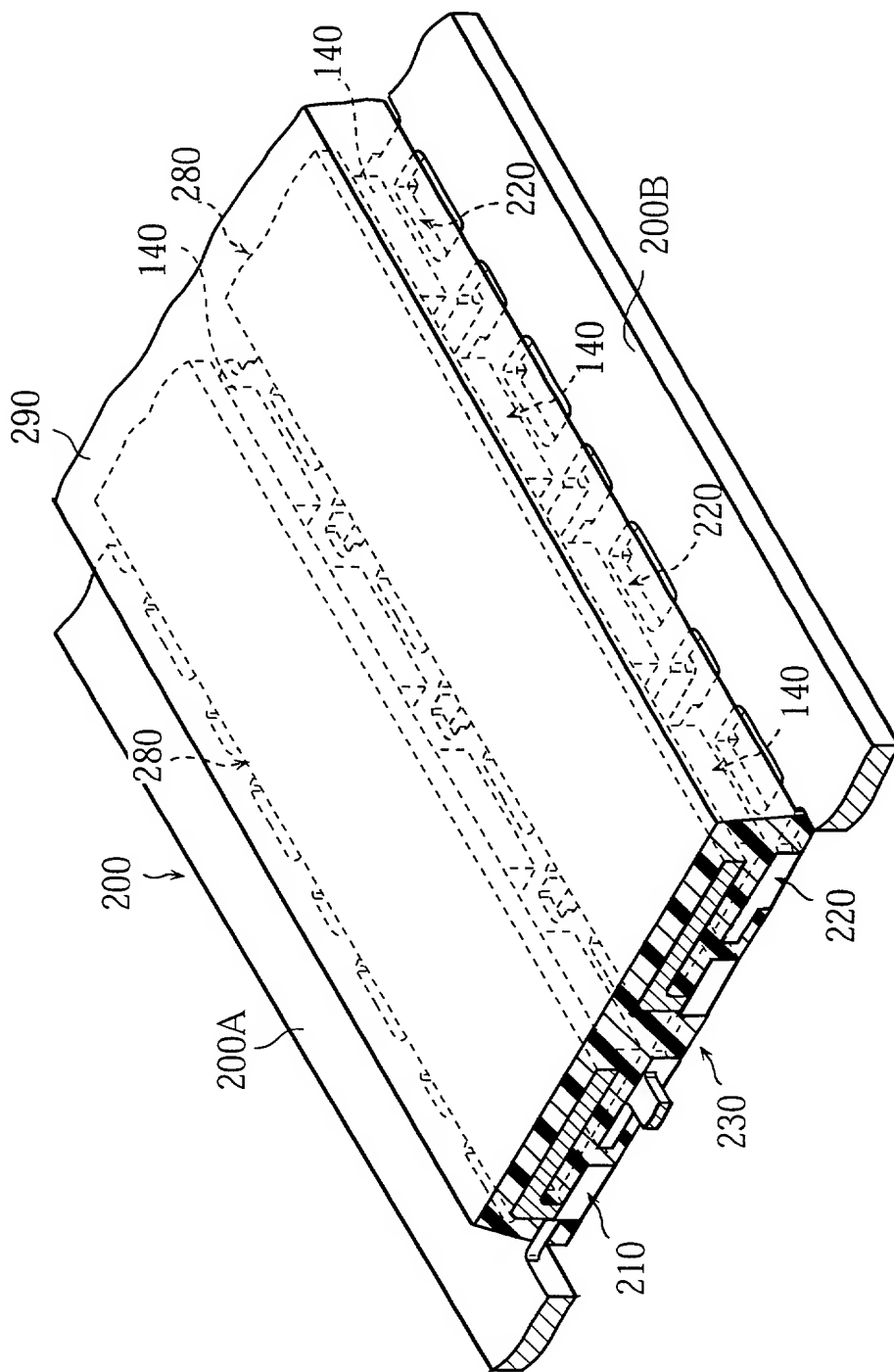


FIG. 9

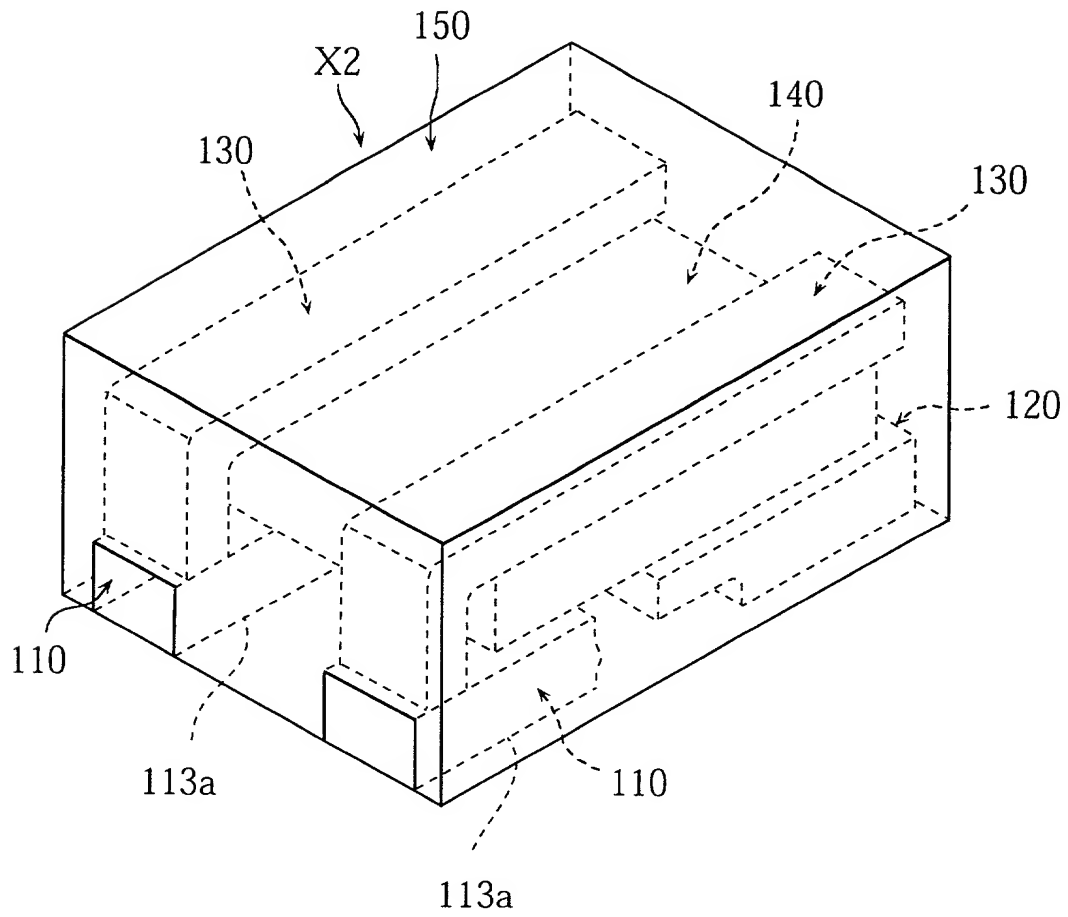


FIG. 10

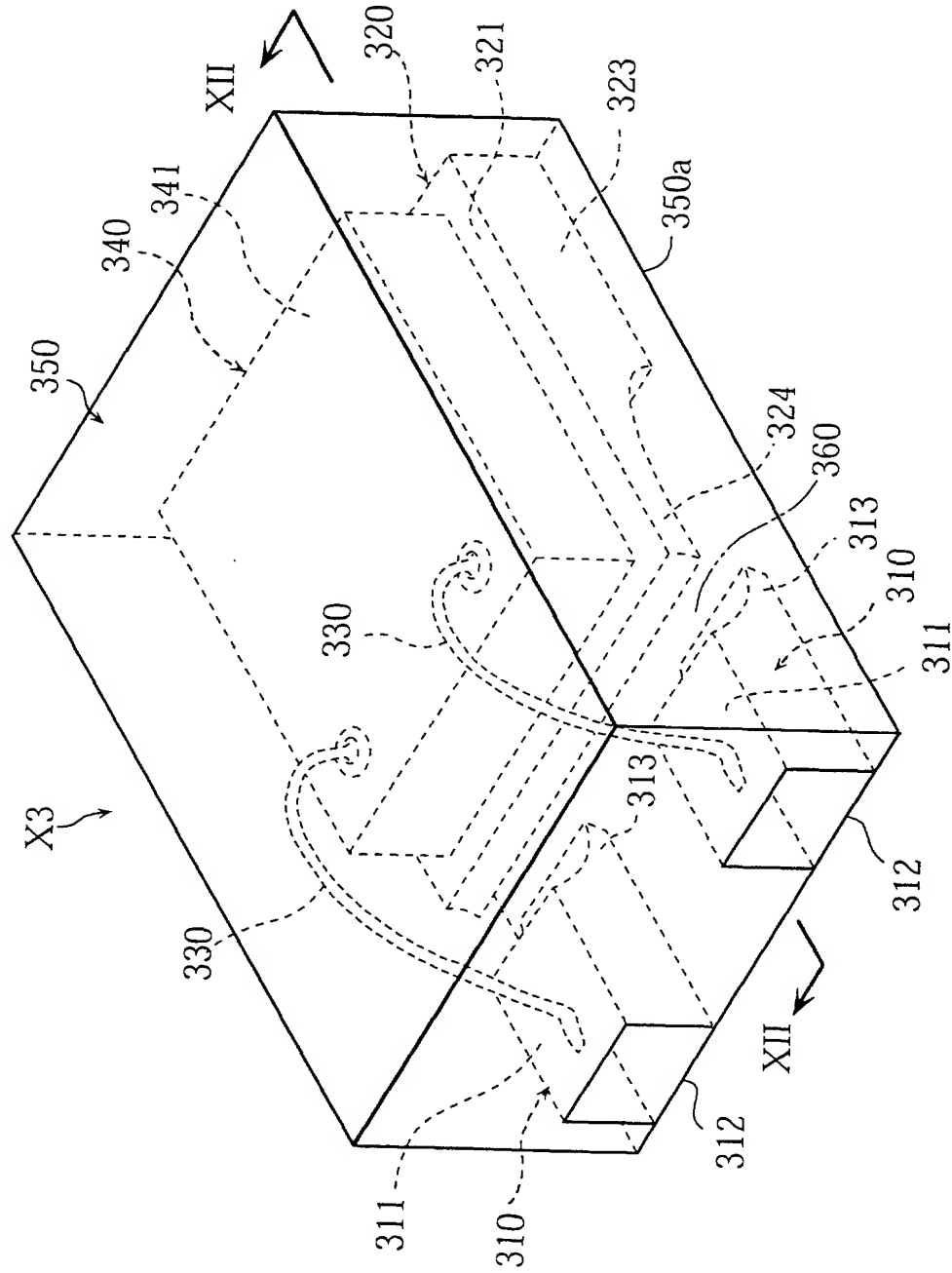


FIG. 13

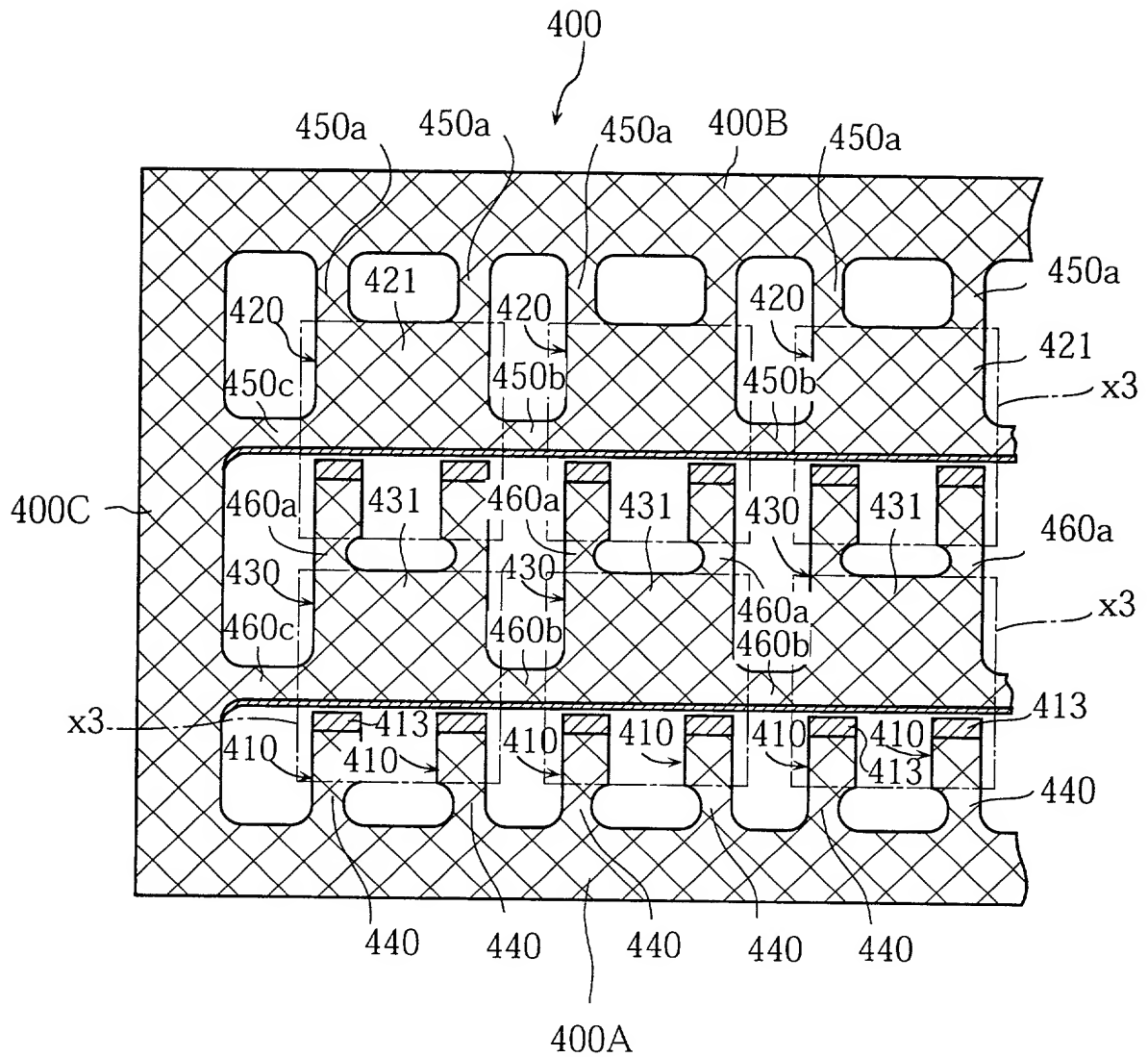


FIG. 14

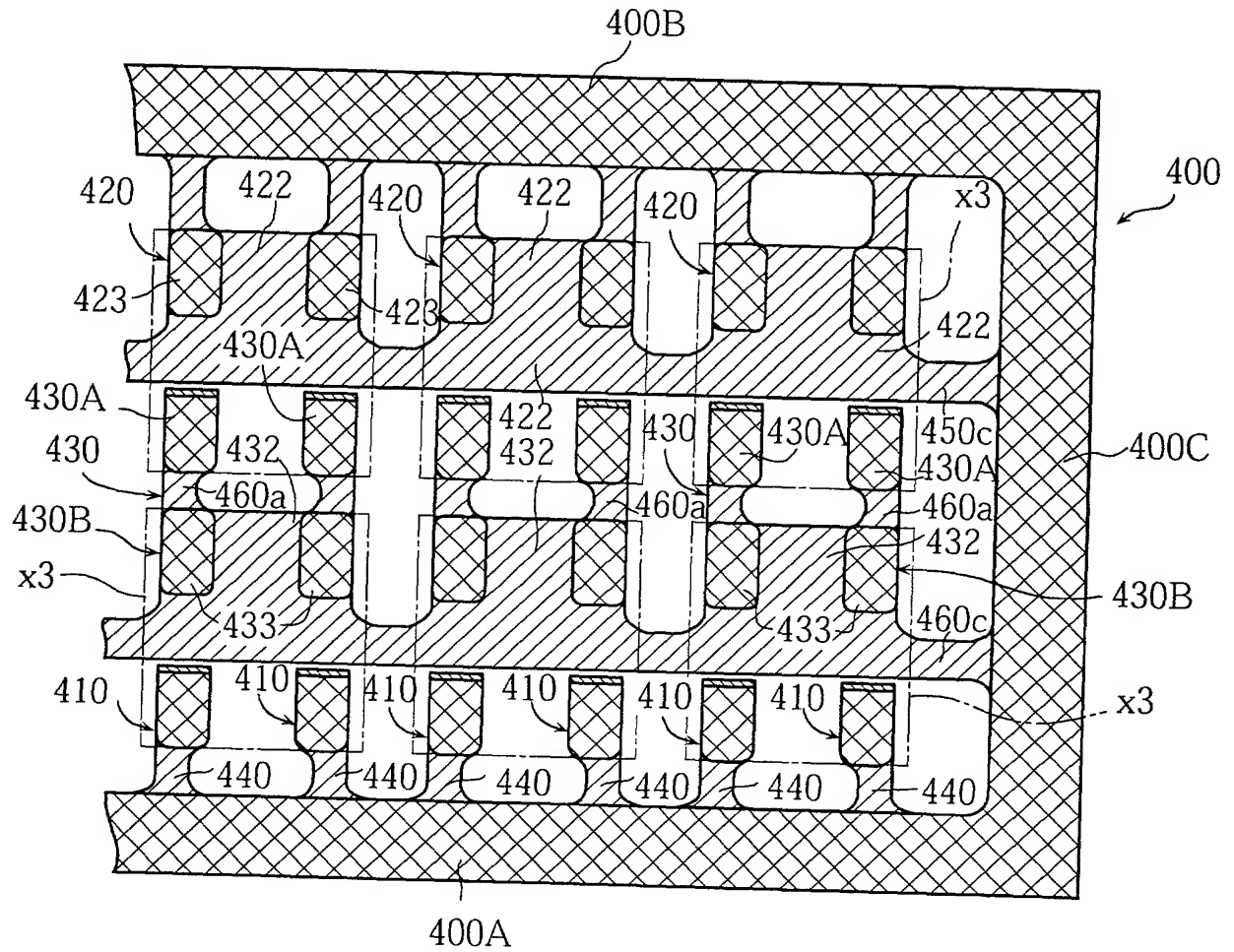


FIG. 15A

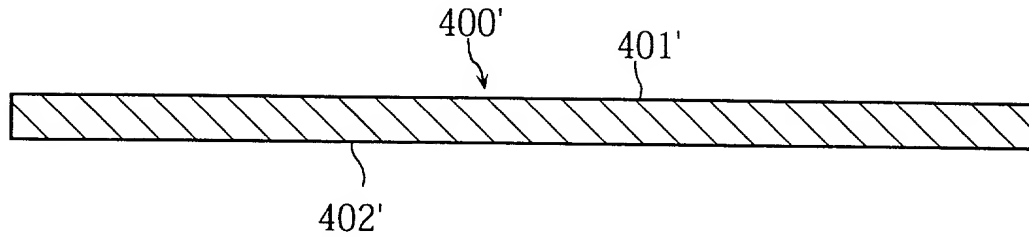


FIG. 15B

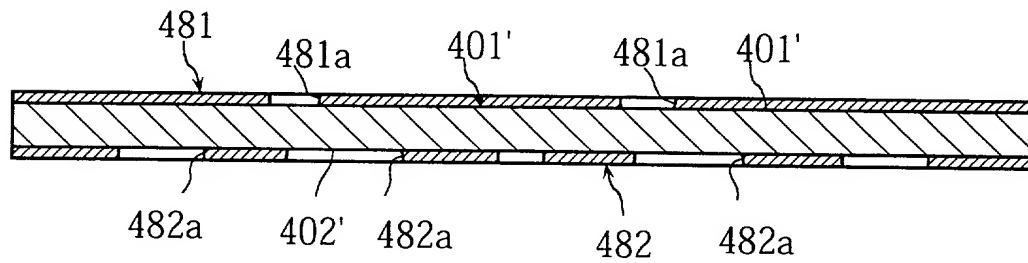


FIG. 15C

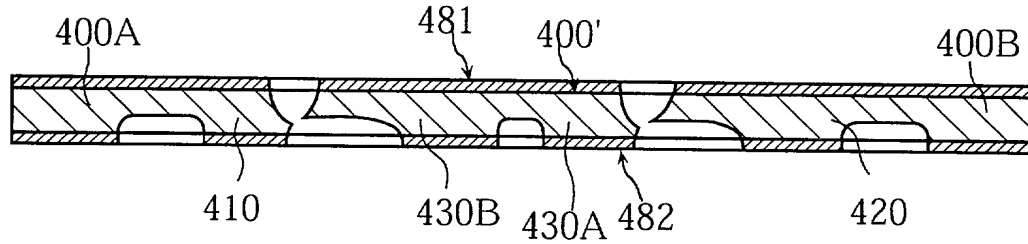


FIG. 15D

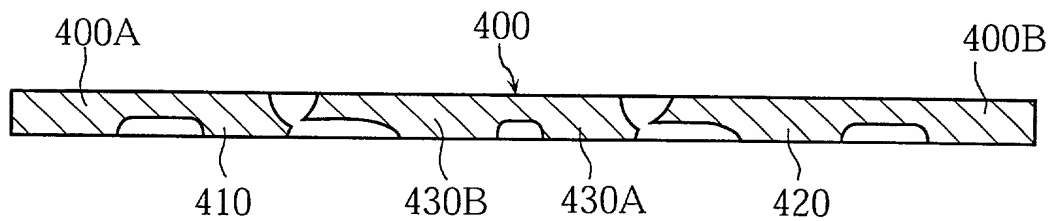


FIG. 16

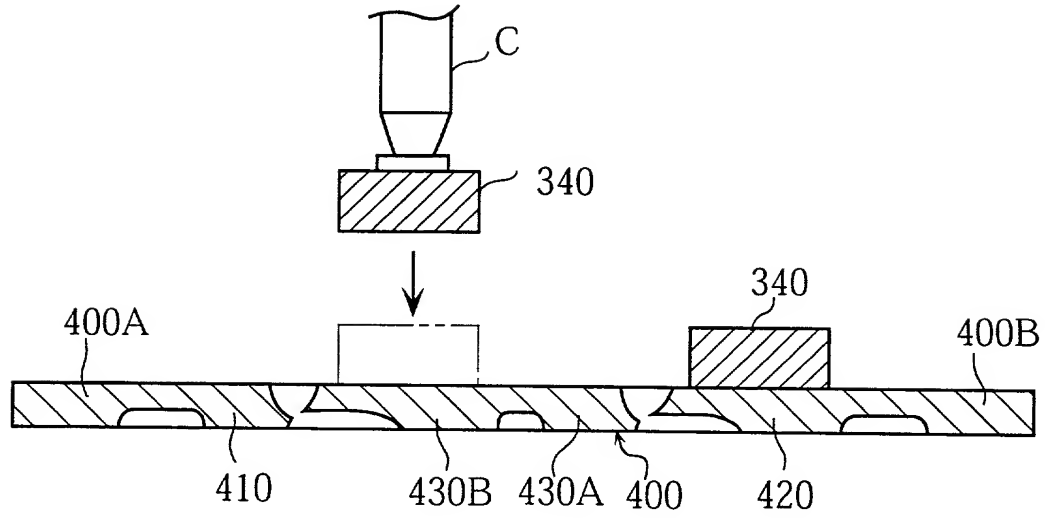
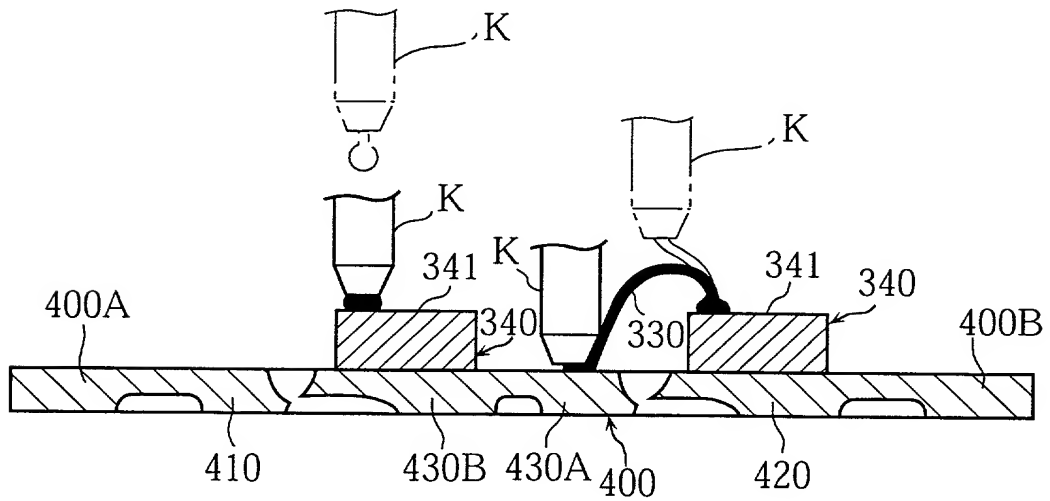


FIG. 17



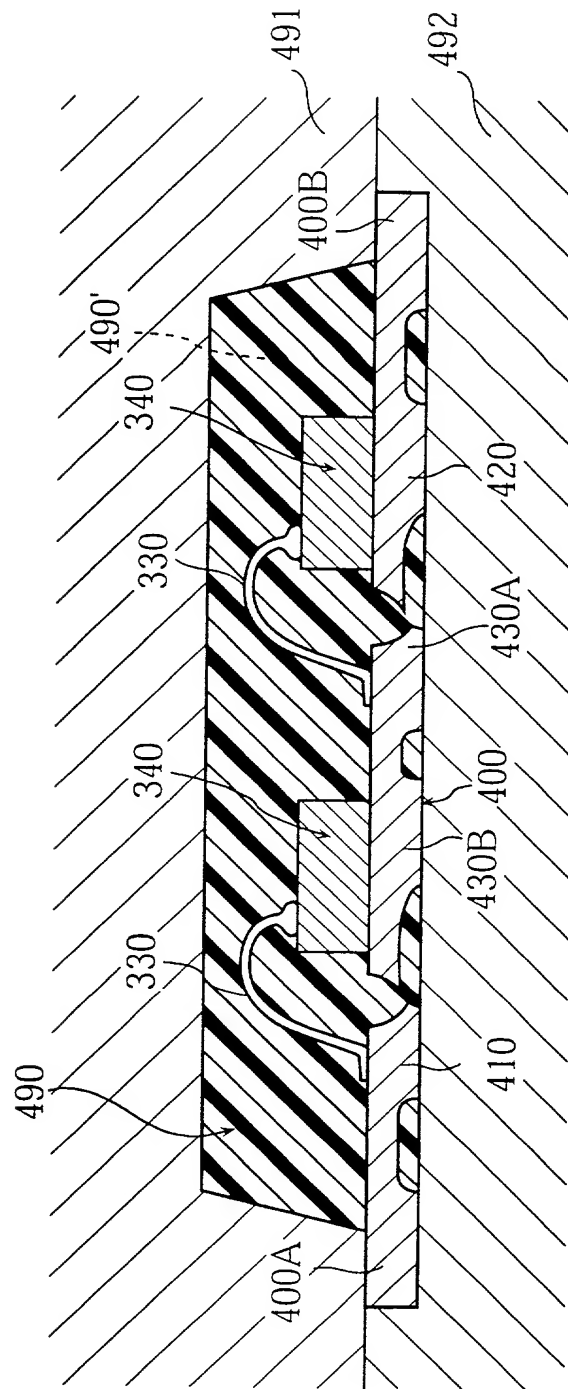
[illegible]

FIG. 21

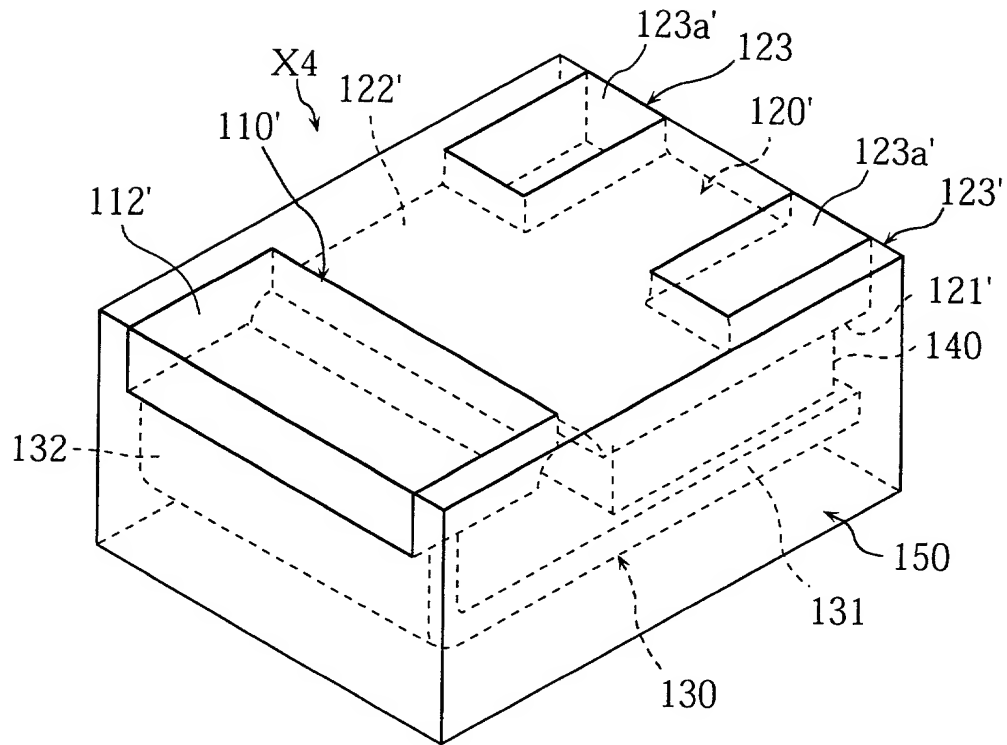


FIG. 22

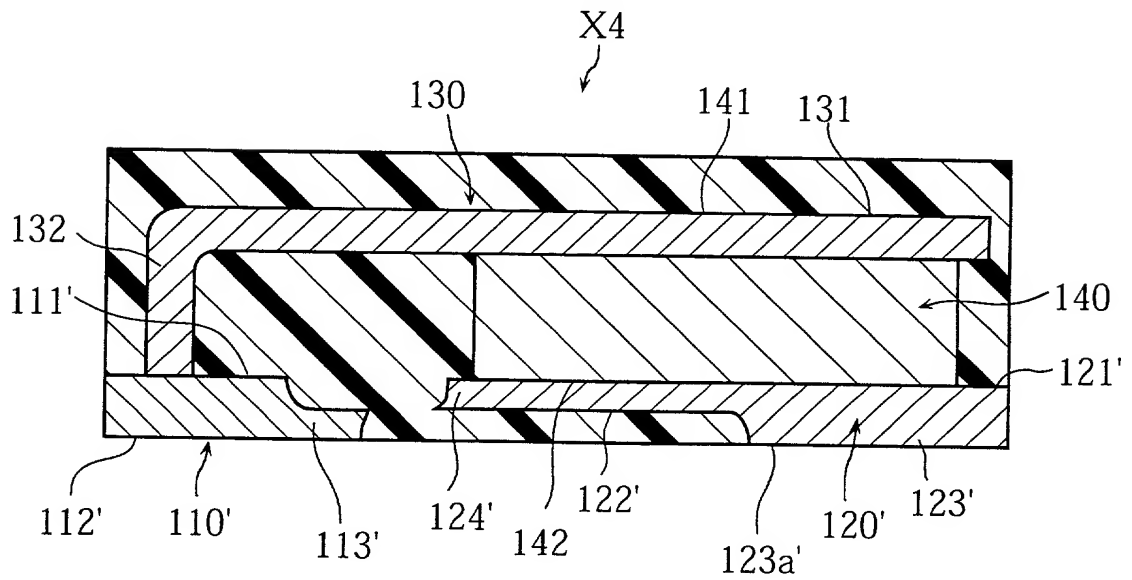


FIG. 24

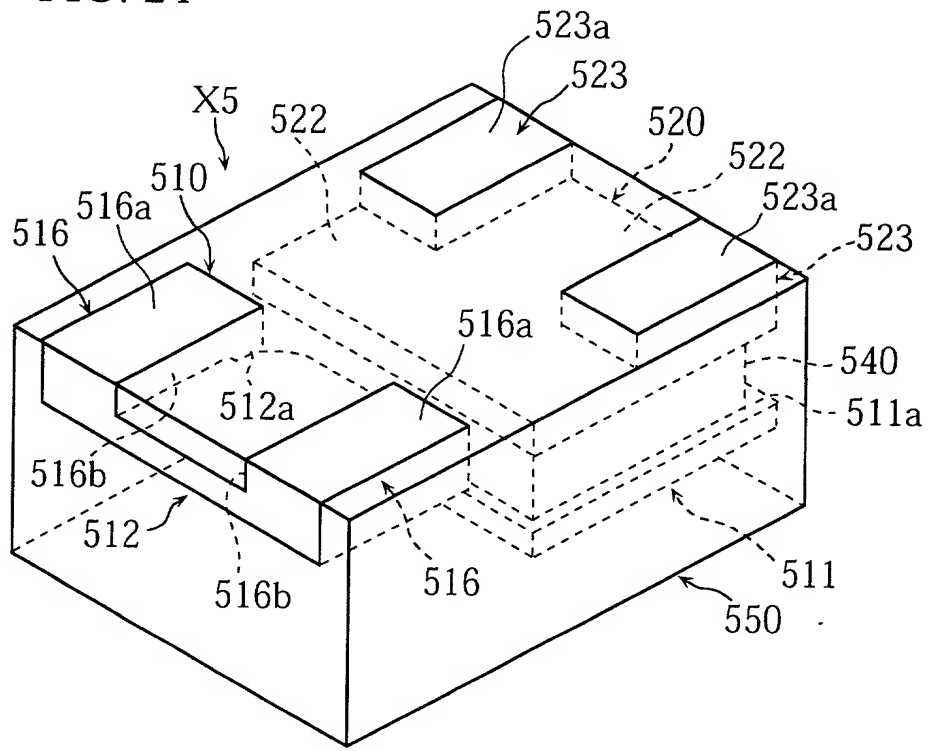
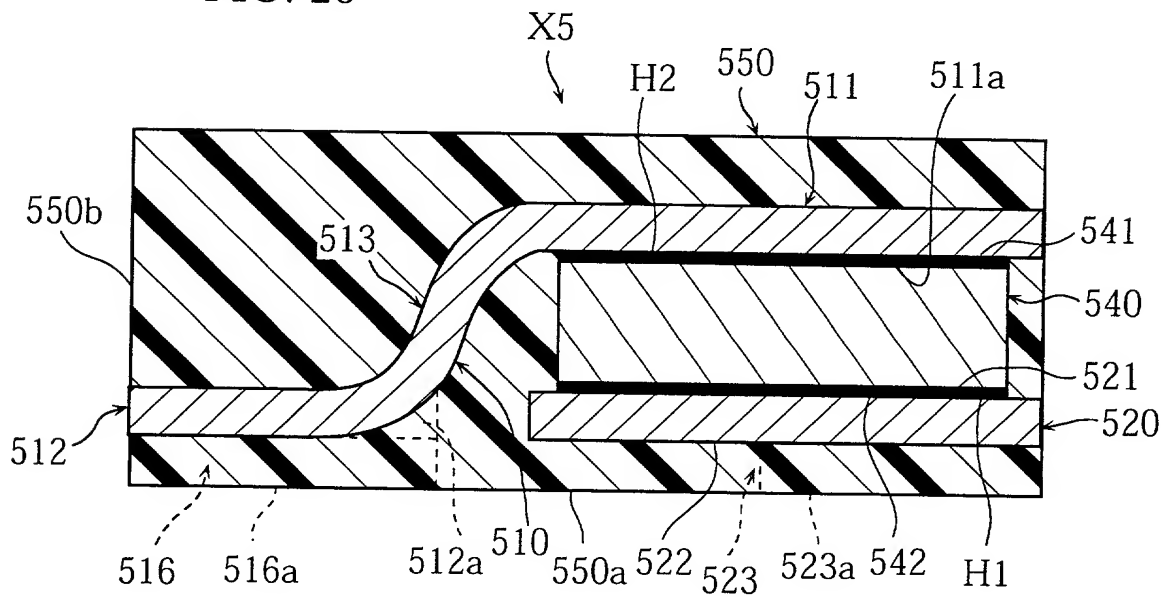


FIG. 25



[illegible]

FIG. 27

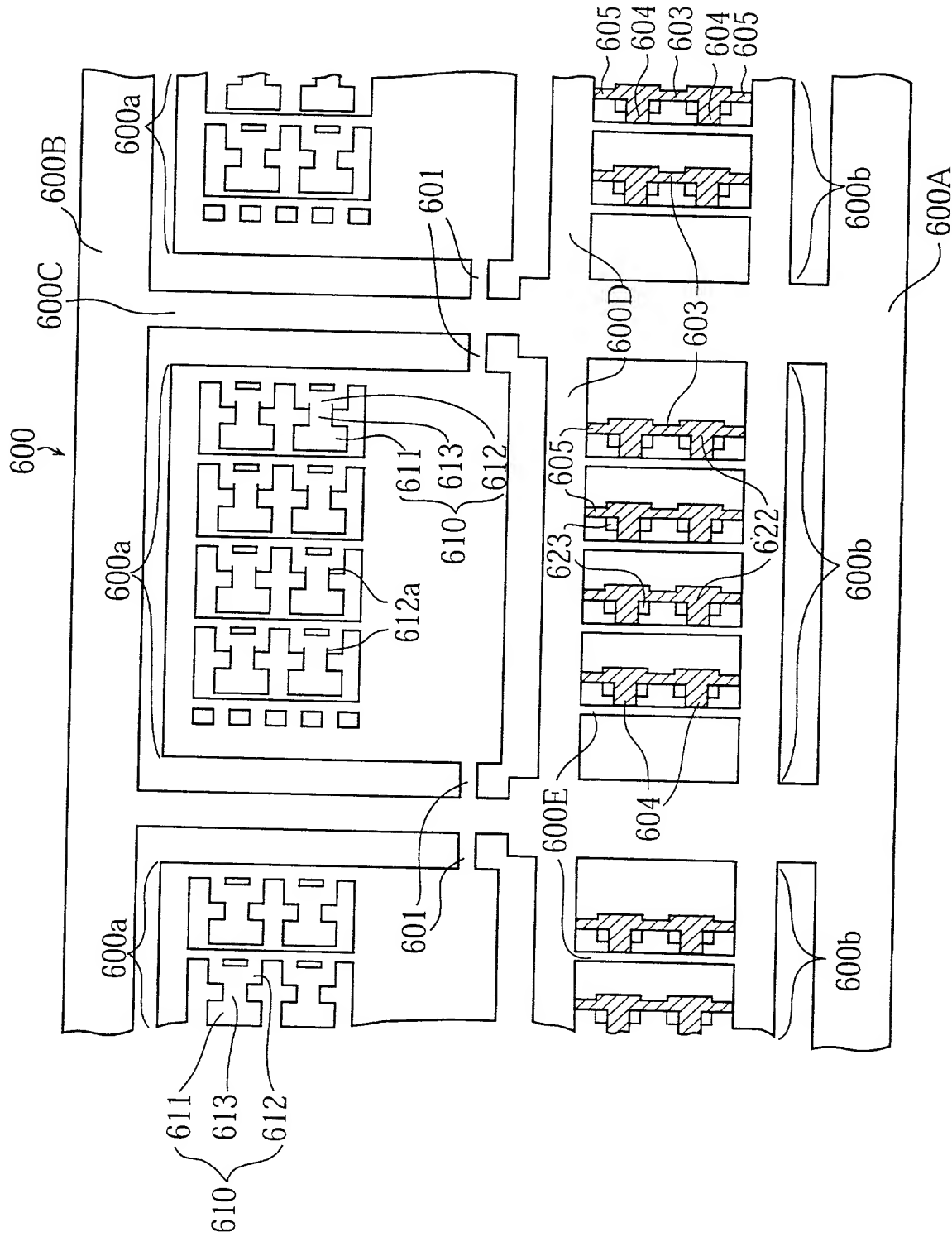
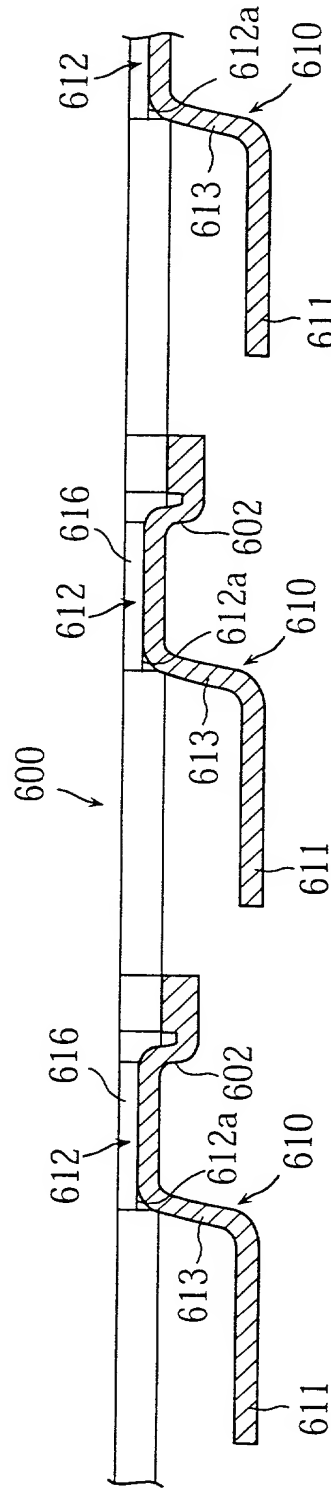


FIG. 28



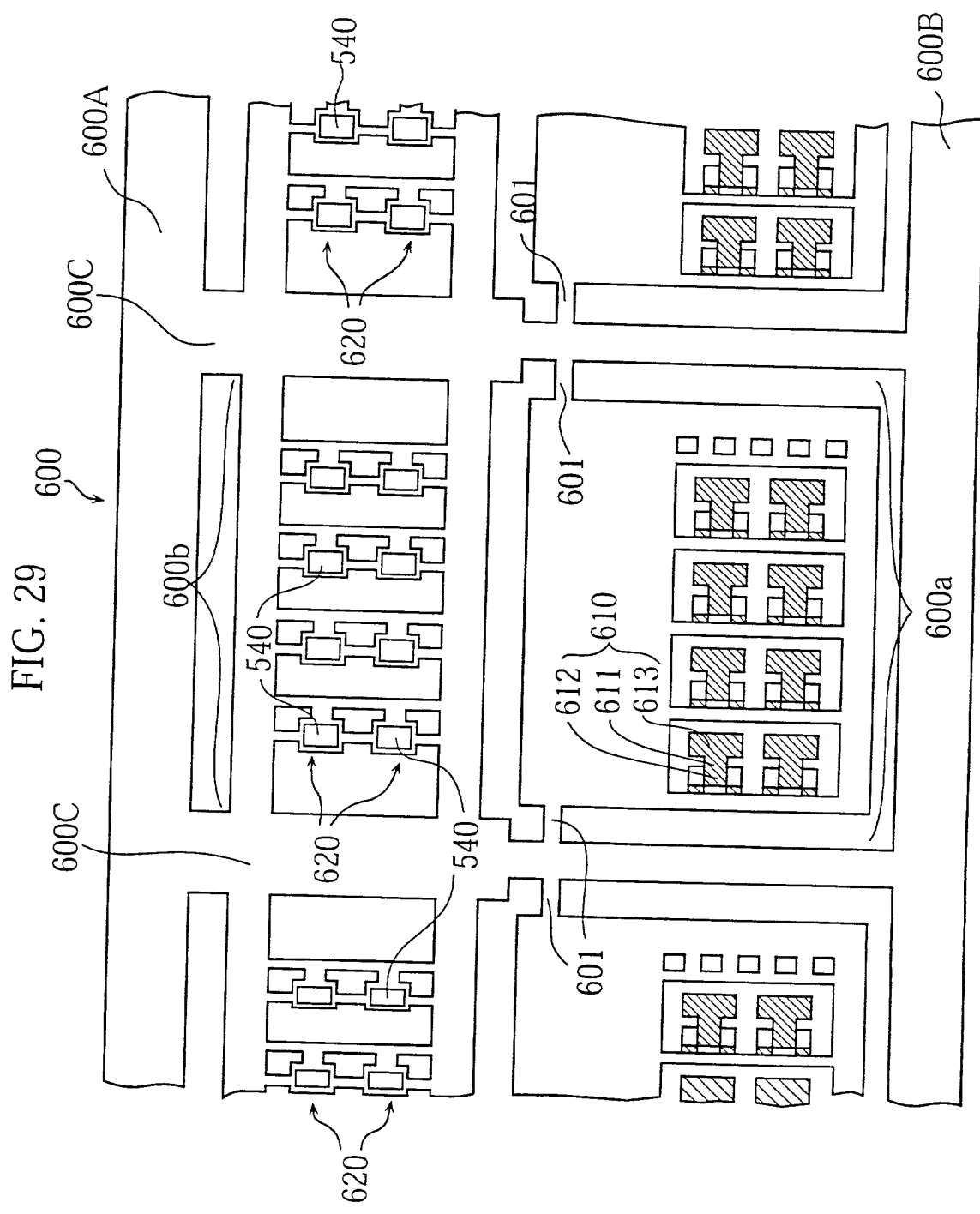


FIG. 30

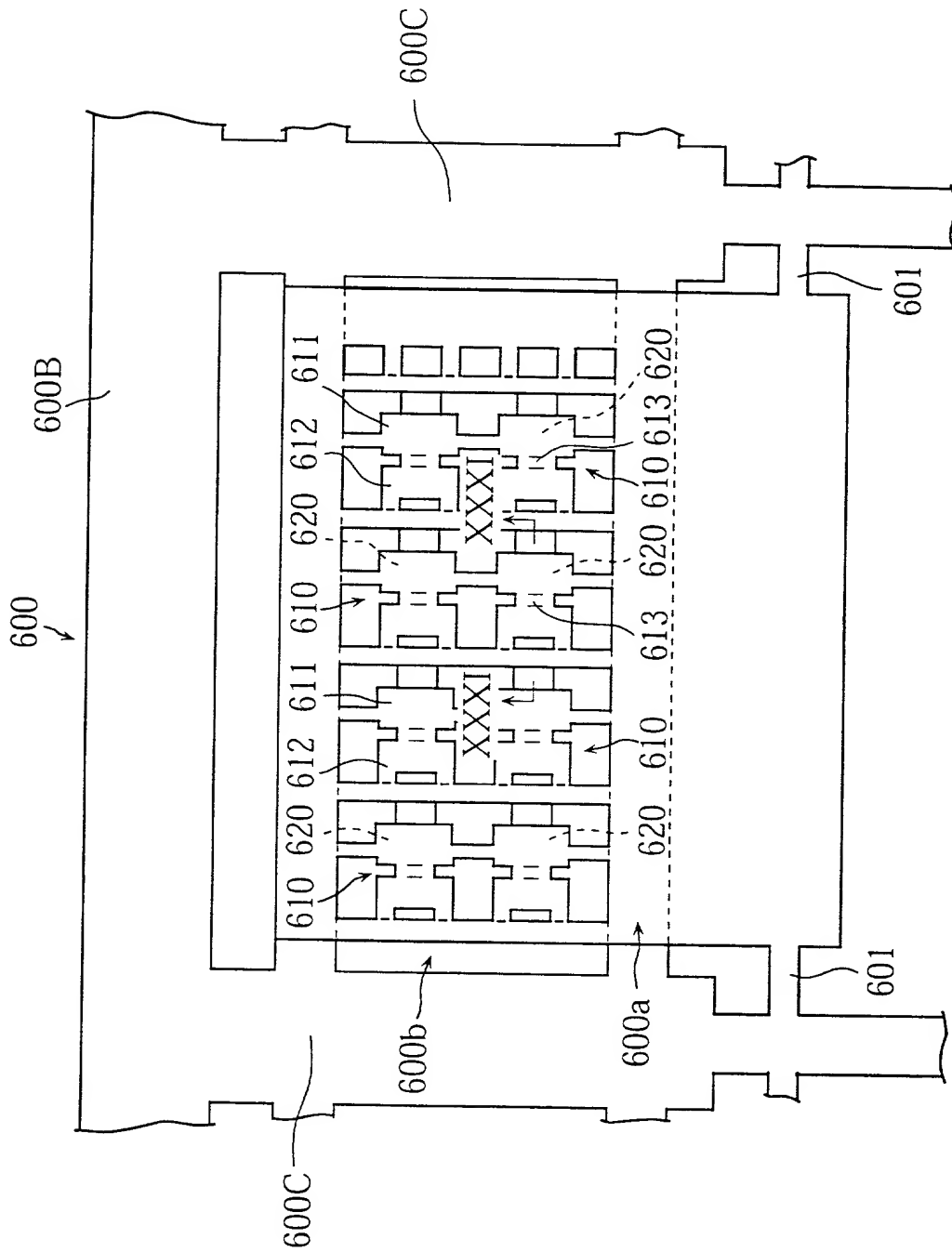
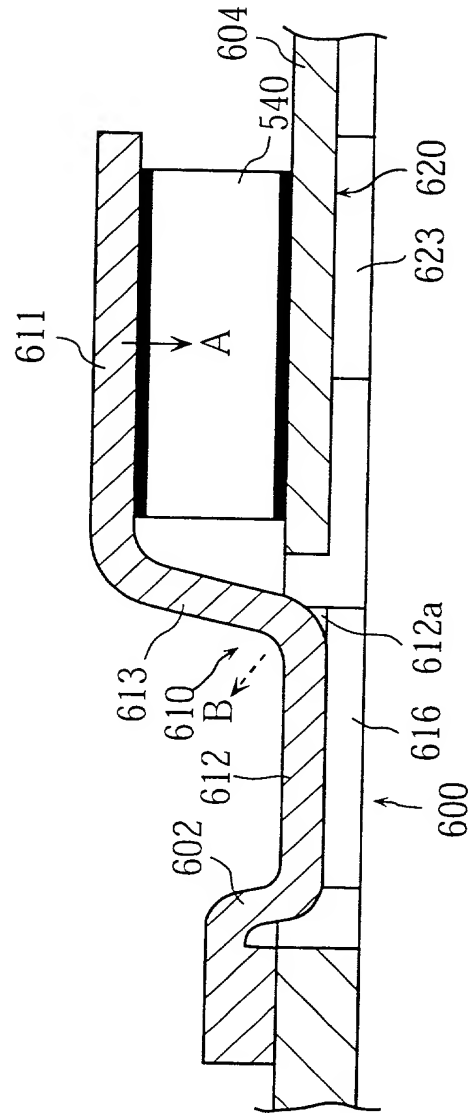


FIG. 31



[illegible]

Figure 1 shows two cross-sectional views of a semiconductor device. The left view shows a device with a central rectangular region 540, surrounded by a layer 550, and a top layer 610 with a pattern 611. A wavy line 613 is at the bottom. The right view shows a similar device with a central rectangular region 540, surrounded by a layer 620, and a top layer 610 with a pattern 611. A wavy line 613 is at the bottom. A label 'DC' is on the left.

FIG. 34

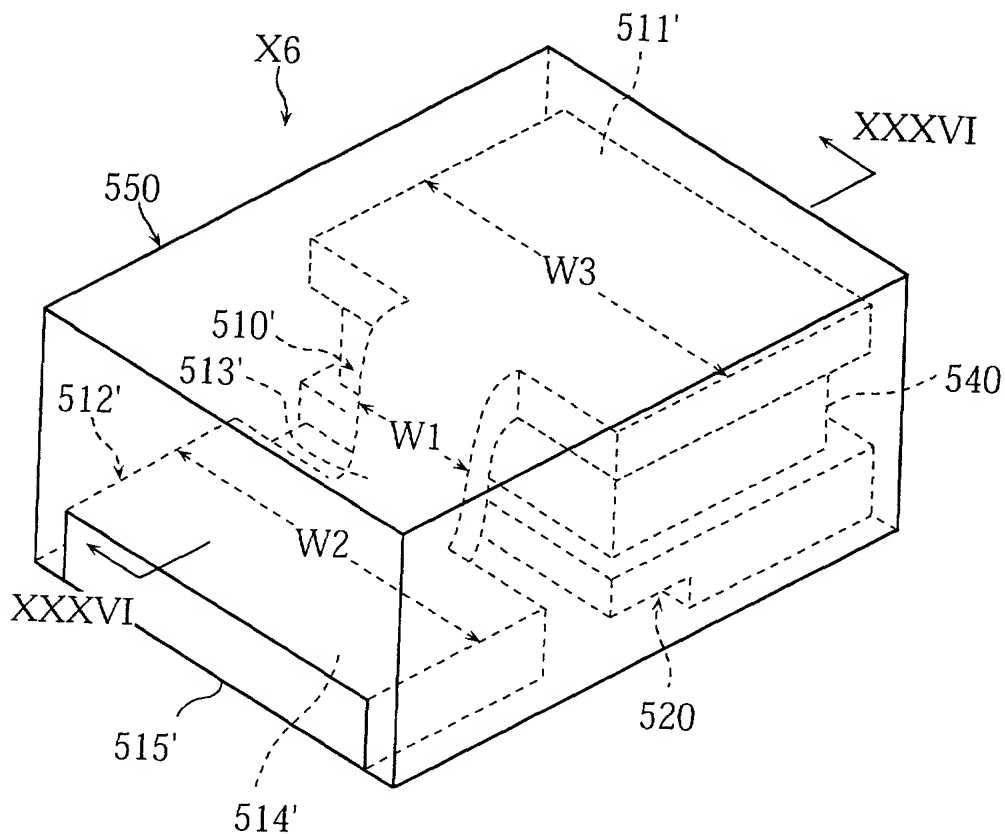


FIG. 35

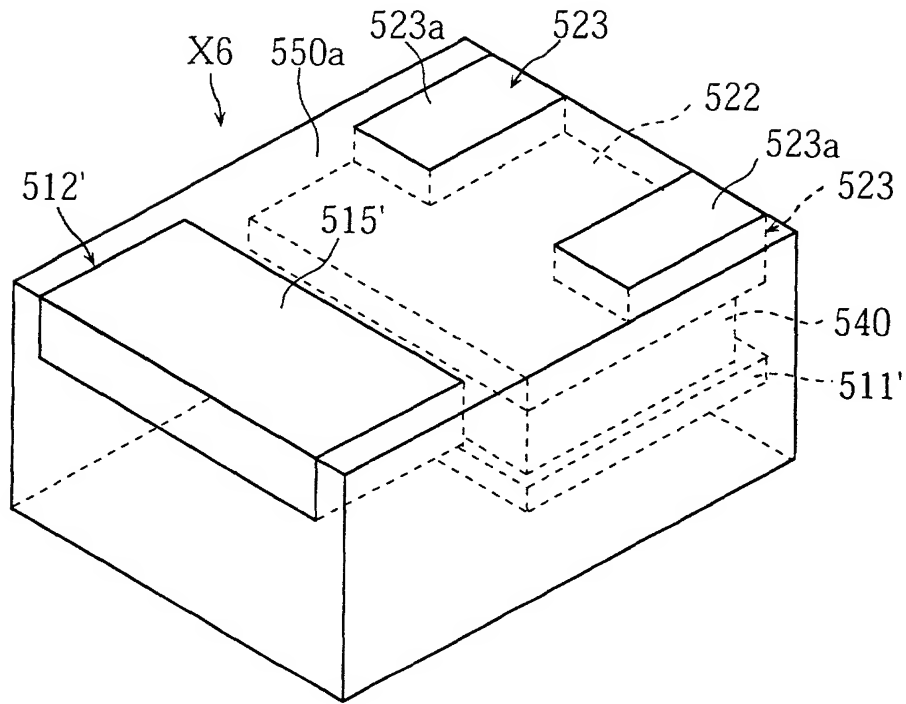


FIG. 36

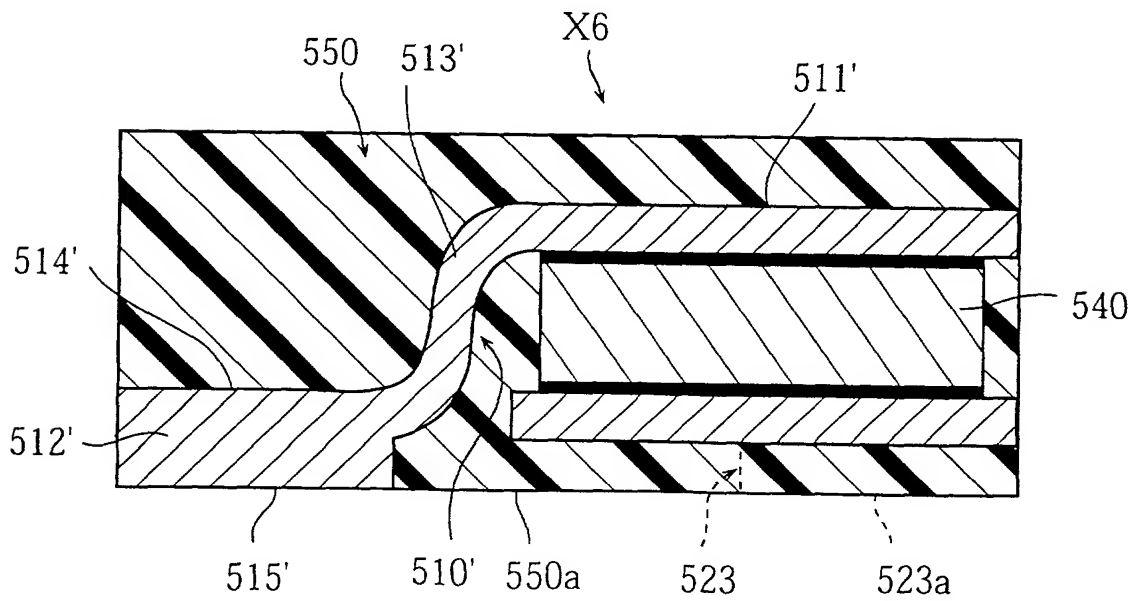


FIG. 37

FIG. 37 is a perspective view of a rectangular box-like structure 710. The structure has a top surface 711, a front face 713, and a right side face 714. A dashed line indicates a width dimension W2 across the top surface. A dashed line indicates a height dimension X7. A dashed line indicates a depth dimension C. A dashed line indicates a length dimension 750. A dashed line indicates a width dimension 740. A dashed line indicates a height dimension 720. A dashed line indicates a depth dimension 742. A dashed line indicates a length dimension 715. A dashed line indicates a width dimension 712. A dashed line indicates a height dimension 714. A dashed line indicates a depth dimension 713. A dashed line indicates a length dimension 710.

G. 38

FIG. 38 is a perspective view of a rectangular block 740. The block has a top surface 750a and a bottom surface 750b. The block is divided into three main sections by two vertical walls 712 and 715. The left section contains a rectangular feature 712. The middle section contains a rectangular feature 715. The right section contains a rectangular feature 722. The feature 722 is further divided into two sub-features 723a and 723. The block is shown with dashed lines indicating internal structure and dimensions. A coordinate system is defined by axes X7, Y7, and Z7. The block is labeled with various reference numerals: 712, 715, 722, 723a, 723, 740, 750a, 711, 741, 713, 714, 715, 716, 717, 718, 719, 720, 721, 722, 723, 724, 725, 726, 727, 728, 729, 730, 731, 732, 733, 734, 735, 736, 737, 738, 739, 740, 741, 742, 743, 744, 745, 746, 747, 748, 749, 750, 751, 752, 753, 754, 755, 756, 757, 758, 759, 760, 761, 762, 763, 764, 765, 766, 767, 768, 769, 770, 771, 772, 773, 774, 775, 776, 777, 778, 779, 780, 781, 782, 783, 784, 785, 786, 787, 788, 789, 790, 791, 792, 793, 794, 795, 796, 797, 798, 799, 800.

FIG. 39

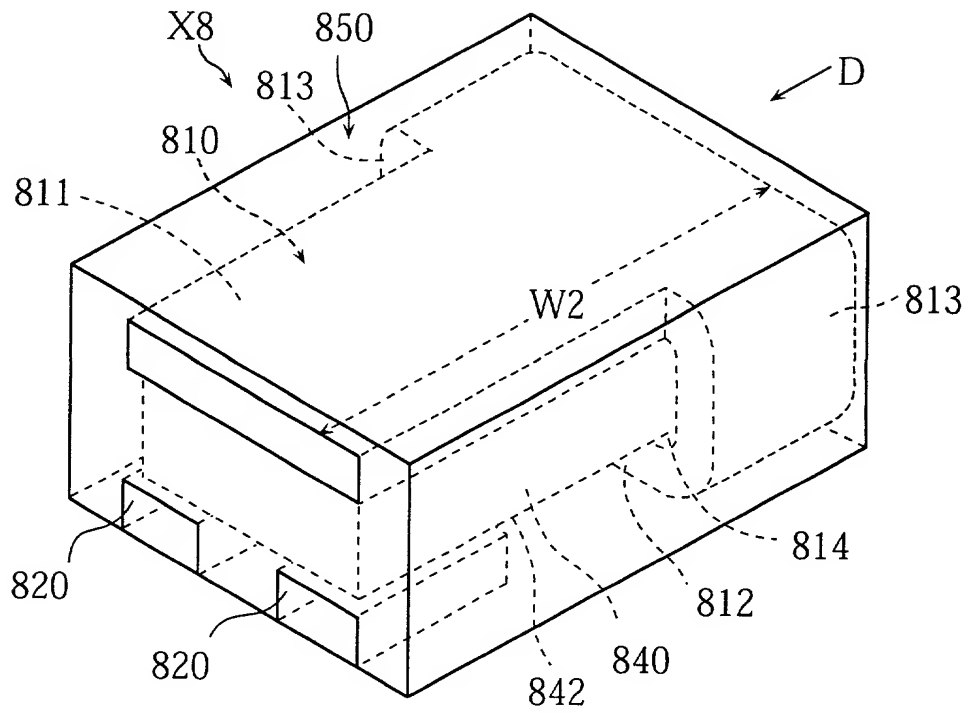


FIG. 40

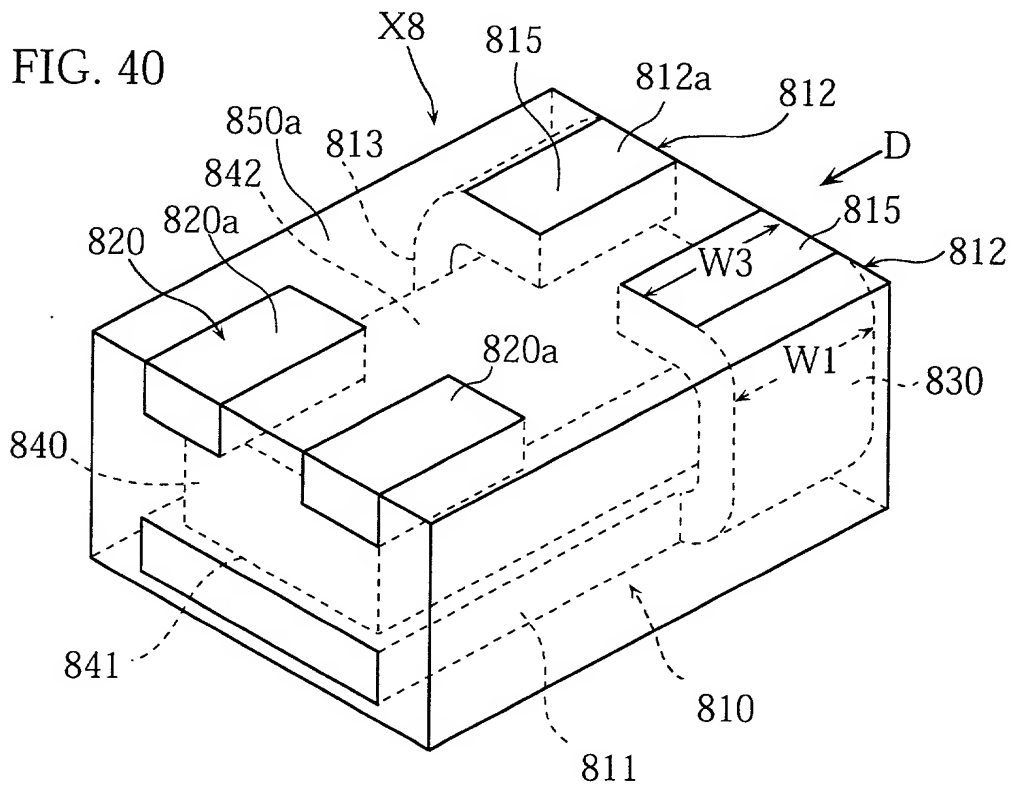


FIG. 41

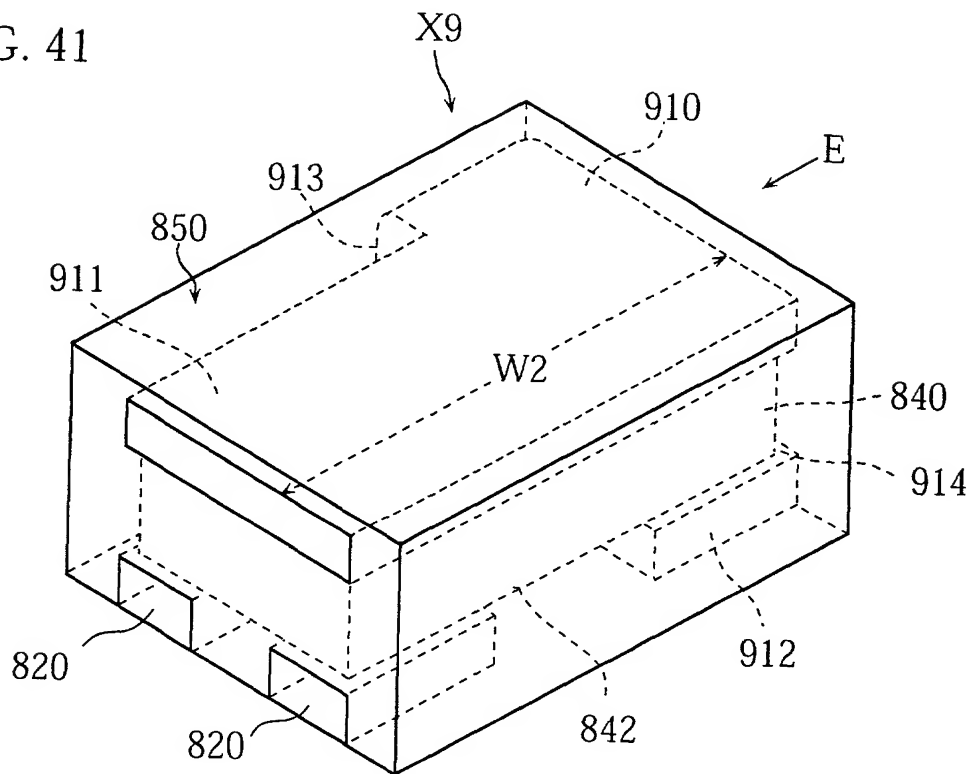


FIG. 42

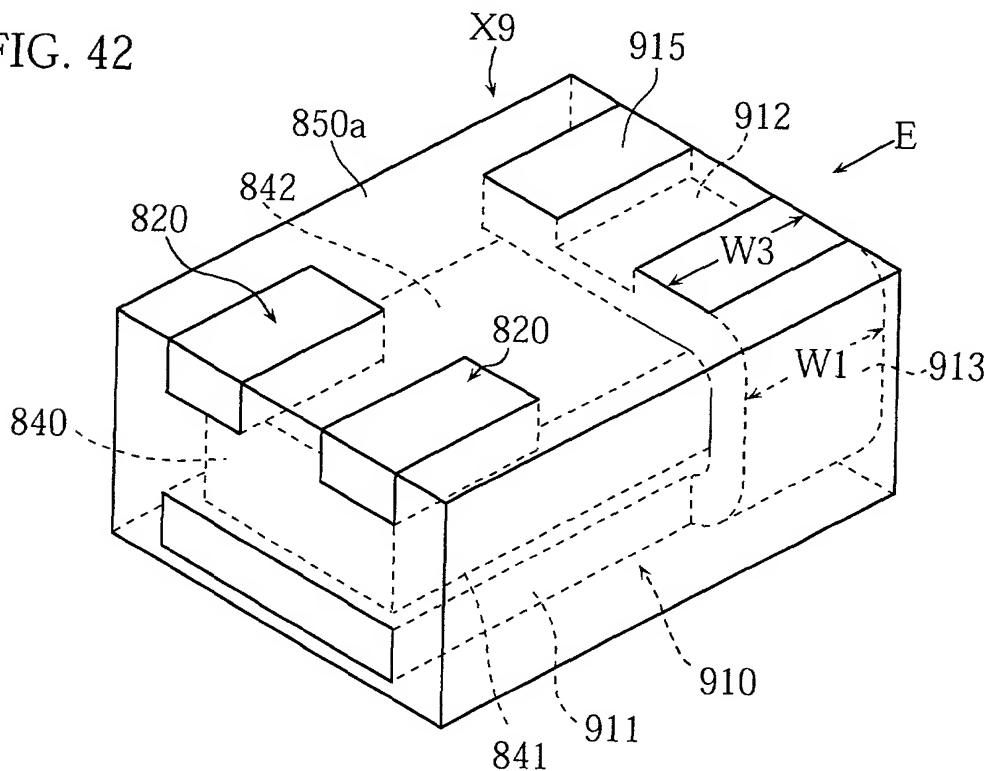


FIG. 45
PRIOR ART

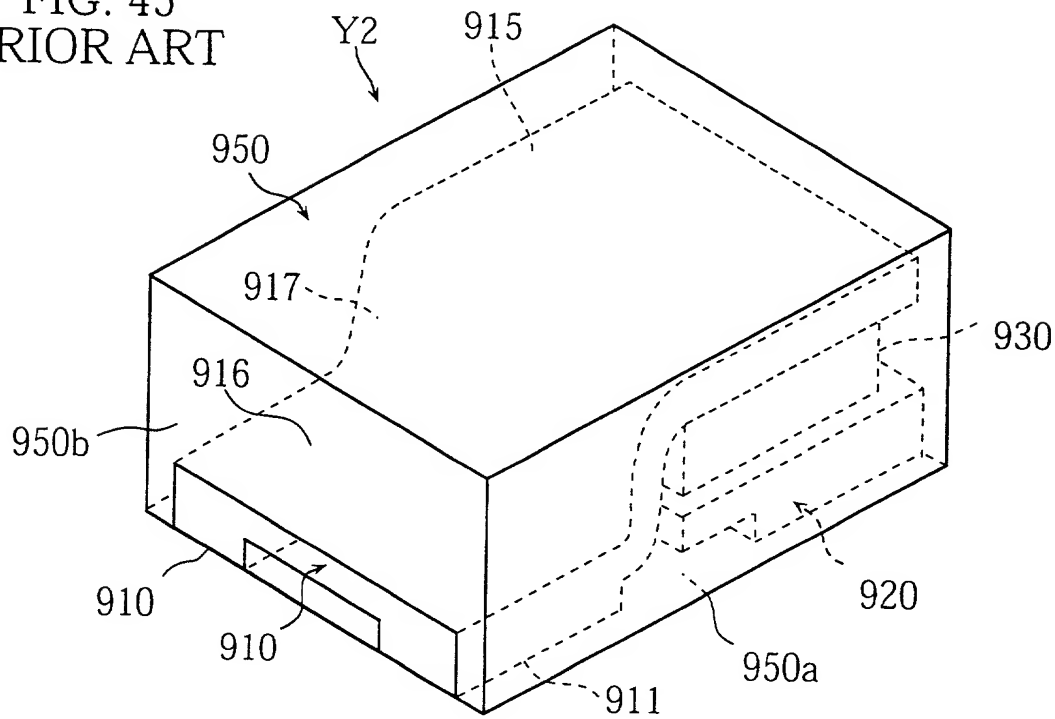


FIG. 46
PRIOR ART

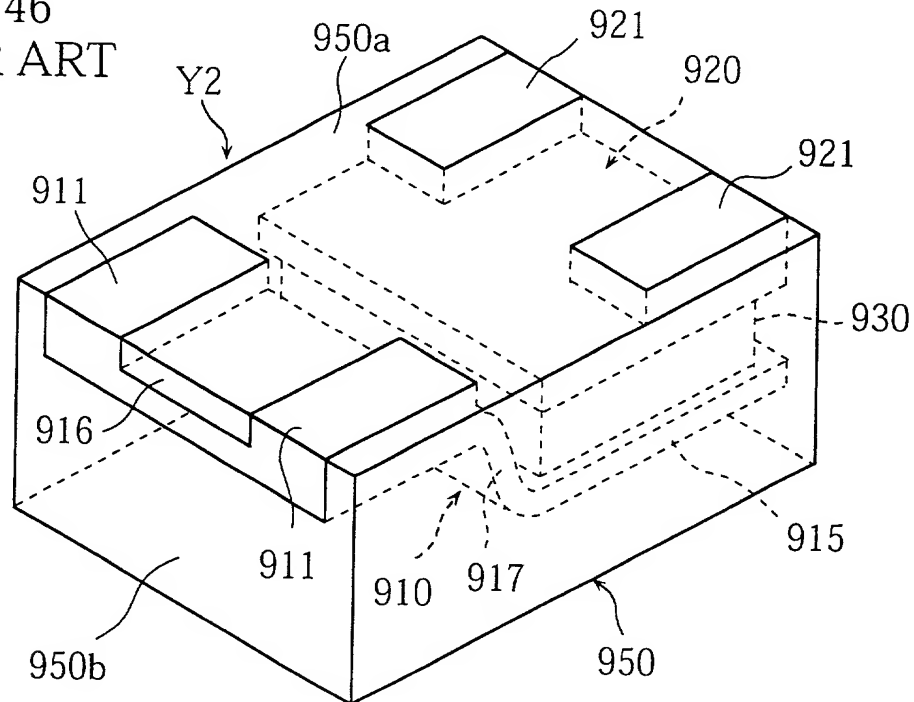
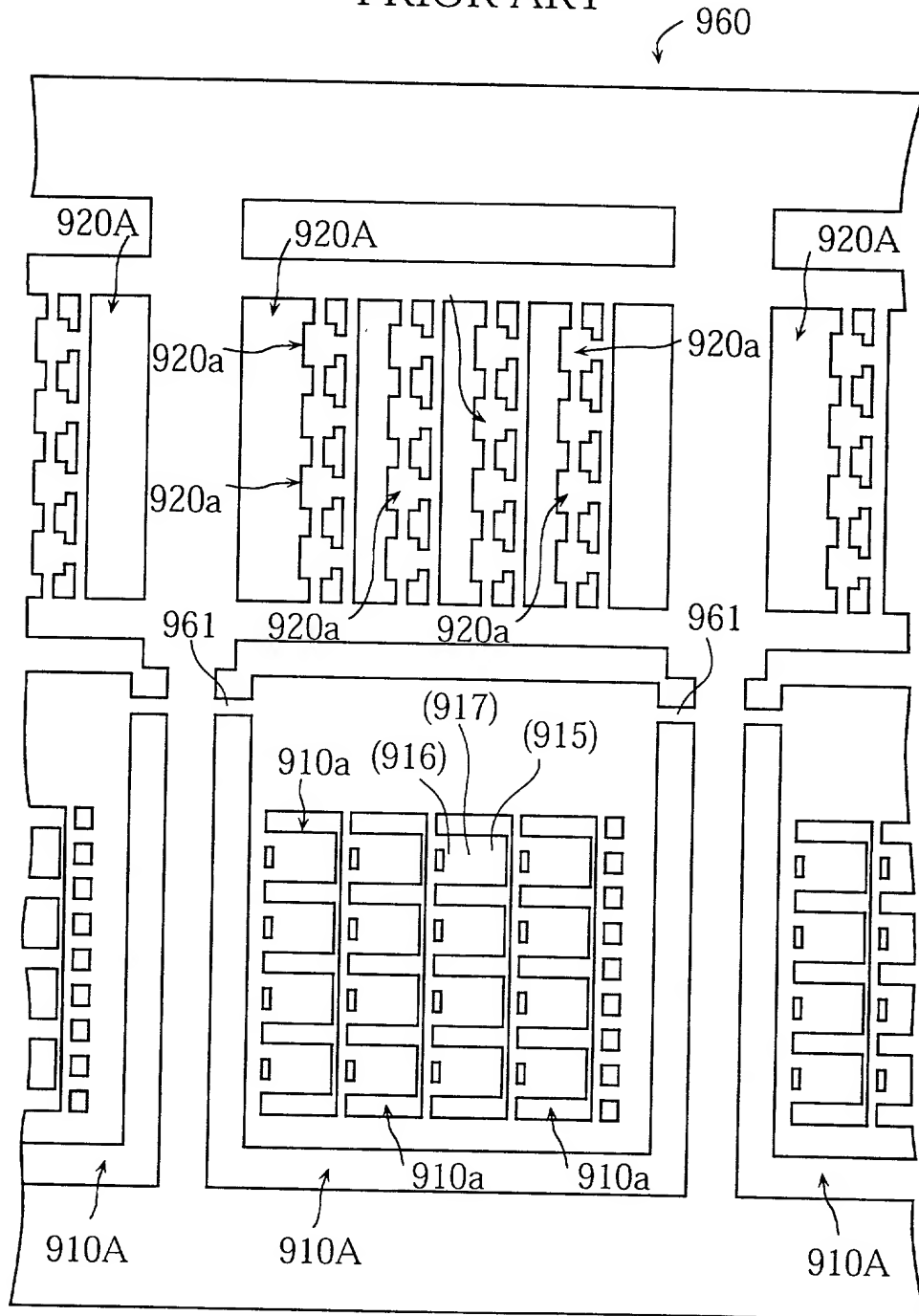


FIG. 47
PRIOR ART



[illegible]